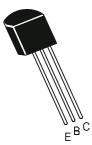


# PNP SILICON PLANAR EPITAXIAL TRANSISTOR



# CSA539

TO-92 Plastic Package

#### **Complementary CSC815**

### Low Frequency Amplifier

## ABSOLUTE MAXIMUM RATINGS (Ta=25°C )

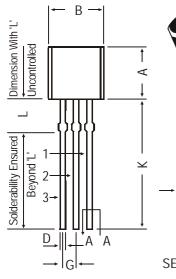
DESCRIPTION	SYMBOL	VALUE	UNIT	
Collector Base Voltage	V <sub>CBO</sub>	60	V	
Collector Emitter Voltage	V <sub>CEO</sub>	45		
Emitter Base Voltage	V <sub>EBO</sub>	5	V	
Collector Current	I <sub>C</sub>	200	mA	
Collector Dissipation	Pc	400	mW	
Operating And Storage Junction T <sub>j</sub> , T <sub>stg</sub>		-55 to +150	°C	
Temperature Range				

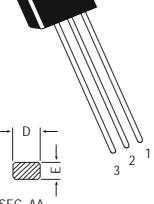
#### ELECTRICAL CHARACTERISTICS (Ta=25°C unless specified otherwise)

DESCRIPTION	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
	N/					
Collector Base Voltage	V <sub>CBO</sub>	I <sub>C</sub> =100μA, I <sub>E</sub> =0	60	-	-	V
Collector Emitter Voltage	$V_{CEO}$	I <sub>C</sub> =10mA, I <sub>B</sub> =0	45	-	-	V
Emitter Base Voltage	$V_{EBO}$	I <sub>E</sub> =10μΑ, I <sub>C</sub> =0	5	-	-	V
Collector Cut off Current	I <sub>CBO</sub>	$V_{CB}$ =45V, $I_{E}$ = 0	-	-	100	nA
Emitter Cut off Current	I <sub>EBO</sub>	$V_{EB}=3V$ , $I_{C}=0$	-	-	100	nA
DC Current Gain	h <sub>FE</sub>	V <sub>CE</sub> =1V, I <sub>C</sub> =50mA	40	-	240	
Base Emitter on Voltage	V <sub>BE</sub> (on)	$V_{CE}$ =1V, $I_{C}$ =10mA	0.60	-	0.90	V
Collector Emitter Saturation Voltage	$V_{\text{CE(sat)}}$	$I_{C}$ =150mA, $I_{B}$ =15mA	-	-	0.5	V
Base Emitter Saturation Voltage	V <sub>BE(sat)</sub>	I <sub>C</sub> =150mA, I <sub>B</sub> =15mA	-	-	1.2	V
Classification		R	0		Y	
h <sub>FE</sub>		40 - 80	70 - 140		120 - 240	

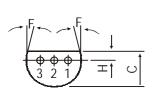
# TO-92 **Plastic Package**

# **TO-92 Plastic Package**





SEC AA

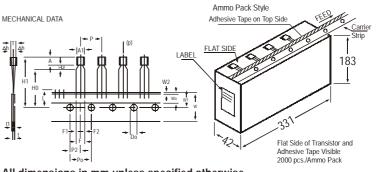


**PIN CONFIGURATION** 

1. COLLECTOR

- BASE 2.
- 3. EMITTER

DIM	MIN.	MAX.				
А	4.32	5.33				
В	4.45	5.20				
С	3.18	4.19				
D	0.41	0.55				
Е	0.35	0.50				
F	5 DEG					
G	1.14	1.40				
Н	1.14	1.53				
К	12.70	_				
L	1.982	2.082				



#### **TO-92 Transistors on Tape and Ammo Pack**

#### All dimensions in mm unless specified otherwise

ITEM		SPECIFICATION					
ITEM	SYMBOL	MIN.	MIN. NOM. MAX. TOL.		TOL .	REMARKS	
BODY WIDTH	A1	4.0		4.8			
BODY HEIGHT	A	4.8		5.2			
BODY THICKNESS PITCH OF COMPONENT	T P	3.9	12.7	4.2	. 1		
FEED HOLE PITCH	Po		12.7		±1 ±0.3	CUMULATIVE PITCH	
FEED HOLE CENTRE TO	10		12.7		10.5	ERROR 1.0 mm/20 PITCH	
COMPONENT CENTRE	P2		6.35		±0.4	TO BE MEASURED AT BOTTOM OF CLINCH	
DISTANCE BETWEEN OUTER					+0.6		
	F		5.08	1	-0.2	AT TOP OF BODY	
COMPONENT ALIGNMENT TAPF WIDTH	∆h W		0 18		±0.5	AT TOP OF BODY	
HOLD-DOWN TAPE WIDTH	Wo		6		±0.3		
HOLE POSITION	W1		9		+0.7 -0.5		
HOLD-DOWN TAPE POSITION	W2		0.5		±0.2		
LEAD WIRE CLINCH HEIGHT	Но		16		±0.5		
COMPONENT HEIGHT	H1			23.25			
LENGTH OF SNIPPED LEADS FFFD HOLF DIAMFTFR	L Do		4	11.0	±0.2		
TOTAL TAPE THICKNESS	DU t		4	1.2	±0.2	t1 0.3 - 0.6	
LEAD - TO - LEAD DISTANCEF1,	F2		2.54	2	+0.4	1 0.0 0.0	
					-0.1		
CLINCH HEIGHT	H2			3			
PULL - OUT FORCE	(P)	6N					

NOTES
1. MAXIMUM ALIGNMENT DEVIATION BETWEEN LEADS NOT TO BE GREATER THAN 0.2 mm.
2. MAXIMUM NON-CUMULATIVE VARIATION BETWEEN TAPE FEED HOLES SHALL NOT EXCEED 1 mm IN 20
DITCHES

PITCHES. HOLDDOWN TAPE NOT TO EXCEED BEYOND THE EDGE(S) OF CARRIER TAPE AND THERE SHALL BE NO

All diminsions in mm.

3.

EXPOSURE OF ADJESIVE.
NO MORE THAN 3 CONSECUTIVE MISSING COMPONENTS ARE PERMITTED.
A TAPE TRAILER, HAVING AT LEAST THREE FEED HOLES ARE REQUIRED AFTER THE LAST COMPONENT.

6. SPLICES SHALL NOT INTERFERE WITH THE SPROCKET FEED HOLES.

# **Packing Detail**

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Qty	Size	Qty	Size	Qty	Gr Wt
TO-92 Bulk TO-92 T&A	1K/polybag 2K/ammo box	J J I	3" x 7.5" x 7.5" 12.5" x 8" x 1.8"		17" x 15" x 13.5" 17" x 15" x 13.5"	80K 32K	23 kgs 12.5 kgs

#### **CSA539**

TO-92 Plastic Package

#### Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



CDIL is a registered Trademark of

Continental Device India Limited C-120 Naraina Industrial Area, New Delhi 110 028, India. Telephone + 91-11-2579 6150, 5141 1112 Fax + 91-11-2579 5290, 5141 1119 email@cdil.com www.cdilsemi.com

CSA539070801

Continental Device India Limited